

## **stuff that's unused as yet**

It is important that you become familiar with all the different kinds and uses of registers available on the AARCH64 ISA. We'll dive into this soon. But first, it is vitally important to expand upon your notion of “variable.”

So far we have restricted our discussion of registers almost exclusively to the **x** registers. These are 8 bytes wide and are used for both signed and unsigned integer operations.